Notice of Allowability	Application No.	Applicant(s)	
	10/632,382	HOU ET AL.	
	Examiner	Art Unit	
	Vincent Q Nguyen	2858	
The MAILING DATE of this communication appearable All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication IGHTS. This application is subject to	olication. If not include will be mailed in due	ed course. THIS
1. This communication is responsive to			
2. The allowed claim(s) is/are <u>1-7</u> .			
3. \boxtimes The drawings filed on <u>09 February 2004</u> are accepted by the	ne Examiner.		
4. ☐ Acknowledgment is made of a claim for foreign priority unexpanded the complex of the complex of the complex of the complex of the priority documents have a complex of the complex of the priority documents have a complex of the complex of the priority documents have a complex of the complex of the priority documents have a complex of the priority documents h	e been received. been received in Application No cuments have been received in this re	national stage applicational stage application of the front (not the d).	quirements NOTICE OF
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. Notice of Informal P 6. Interview Summary Paper No./Mail Dat 7. Examiner's Amendn 8. Examiner's Stateme 9. Other	(PTO-413), e nent/Comment	owance 9 162/2004

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DETAILED ACTION

Allowable Subject Matter

- 1. Claims 1-7 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:

The prior art of record does not teach or suggest a device having a timing circuit for readying an out-of-step logic circuit if the multi-phase external fault remains for a preselected period of time; a circuit for declaring an out-of-step condition and for blocking selected distance elements of the protective relay if the positive sequence impedance remains inside a selected impedance plane boundary of protection for a qualified period of time after clearing of the multi-phase external fault, as recited in the independent claim 1, and in combination of the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Contact Information

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vincent Q Nguyen whose telephone number is (571) 272-2234. The examiner can normally be reached on 8:30-5:00.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, N. Le can be reached on (571) 272-2233. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Vincent Q Nguyen Patent Examiner Art Unit 2858

September 2, 2004